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INFORMATION DISCLOSURE STATEMENT	Applicants:	Kianian et al
	Application No.	10/105,741
	Filed:	March 20, 2002
	For:	A Semiconductor Memory Array Of Floating Gate Memory Cells With Buried Bit-Line And Raised Source Line
	Group Art Unit:	2815
	Examiner:	George C. Eckert II
	Attorney Docket No.:	2102397-992110

Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450		
Dear Sir:		
In accordance with the provisions of 37 C.F.R. § 1.56(a) and 37 C.F.R. § 1.97, Applicant(s) hereby make of record the references listed on the accompanying Form PTO-1449 for consideration by the Examiner in connection with the examination of the above-identified patent application.		
This Information Disclosure Statement:		
(a)	<input checked="" type="checkbox"/>	accompanies an RCE patent application submitted herewith.
(b)	<input type="checkbox"/>	is filed within three (3) months of the Filing Date or before the mailing date of a First Office Action on the merits; OR
(c)	<input type="checkbox"/>	after the period defined in (b) but before the mailing date of a Final Rejection or Notice of Allowance, OR
(d)	<input type="checkbox"/>	is filed after the first Office Action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned prior to the mailing date of either a final rejection or a notice of allowance, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below OR
(e)	<input type="checkbox"/>	is filed after the mailing date of either a final rejection or a notice of allowance, and the issue fee has not been paid, and is accompanied by the requisite petition fee (\$130) set forth in 37 CFR § 1.17(l)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.
As required under § 1.97(e), Applicants, through the undersigned, hereby state either that [check the appropriate space]:		
(f)	<input type="checkbox"/>	Each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of the Information Disclosure Statement; or
(g)	<input type="checkbox"/>	No item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this Statement after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

It is respectfully requested that each of the references shown on the attached Form PTO-1449 be made of record in this application. Copy of the non-patent reference is enclosed.

The Commissioner is authorized to charge any deficiencies and credit any overpayment of fees to our Deposit Account No. 07-1896.

Respectfully submitted,

Date: August 3, 2004

GRAY CARY WARE & FREIDENRICH LLP

By: 
Reg. No. Alan A. Limbach (Reg. No. 39,749)
Attorneys for Applicant(s)
2000 University Avenue
East Palo Alto, CA 94303-2248
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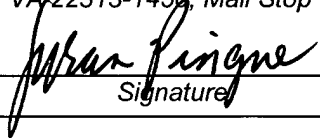
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August 3, 2004
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

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Application Number

10/105,741

Filing Date

March 20, 2002

First Named Inventor

Kianian et al.

Art Unit	Unit	Lesson	Activity	Assessment
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2815

Examiner Name

George C. Eckert II

Attorney Docket Number

2102397-992110

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

**Examiner
Signature**

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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Complete if Known

<i>Application Number</i>	10/105,741
<i>Filing Date</i>	March 20, 2002
<i>First Named Inventor</i>	Sohrab Kianian
<i>Art Unit</i>	2815
<i>Examiner Name</i>	George C. Eckert II
<i>Attorney Docket Number</i>	2102397-992110

Sheet	2	of	2
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NON PATENT LITERATURE DOCUMENTS

Examiner
Initials*Cite
No.¹

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

 τ^2

1 HAYASHI, et al., "A Self-Aligned Split-Gate Flash EEPROM Cell With 3-D Pillar Structure," 1999 Symposium on VLSI Technology Digest of Technical Papers, pp. 87-88, Center for Integrated Systems, Stanford University, Stanford, CA 94305, USA.

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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